02:00.0 is our target pci passthrough device.

VBoxDbg> info pci

……

    02:00:00 pciraw (PASSTHROUGH): 1002-665c IRQ5

            MMIO64 PREFETCH region #0: 0..10000000

            MMIO64 PREFETCH region #2: 10000000..10800000

            IO region #4: e000..e100

            MMIO32 region #5: 10800000..10840000

            MMIO32 PREFETCH region #6: fe940000..fe960000

            Command: 03 04, Status: 10 00

            Bus master: No

VBoxDbg>

dbf event: Fatal error! (hyper)

.eax=00000000 .ebx=00000000 .ecx=00000000 .edx=00000000 .esi=00000000 .edi=00000000

.eip=00000000 .esp=ff620000 .ebp=00000000 .iopl=0 nv up di pl nz na pe nc

.cs=0000 .ds=0000 .es=0000 .fs=0000 .gs=0000 .ss=0000              .eflags=00000000

Failed to disassemble instruction, skipping one byte.

u: error: Too many disassembly failures. Giving up: VERR\_PAGE\_TABLE\_NOT\_PRESENT

VBoxDbg> info mmio

MMIO ranges (pVM=00007fe1cc022000)

GC Phys Range                     pDevIns          Read             Write            Fill             pvUser           Description

00000000000a0000-00000000000bffff 00007fe1d01f4000 00007fe19757a320 00007fe197579170 00007fe197579920 0000000000000000 VGA - VGA Video Buffer

                               R0 00007fe1d01f4000 ffffffffa0dc7f50 ffffffffa0dc8140 ffffffffa0dc7840 0000000000000000

                               RC 00000000 00000000 00000000 00000000 00000000

00000000dc000000-00000000dfffffff 00007fe1bdcd3180 00007fe19756f1a0 00007fe19756f320 0000000000000000 0000000000000000 MCFG ranges

                               R0 00007fe1bdcd3180 ffffffffa0dc5a60 ffffffffa0dc59d0 0000000000000000 0000000000000000

                               RC 00000000 00000000 00000000 00000000 00000000

00000000f0000000-00000000f001ffff 00007fe1bdd21d20 00007fe1976049b0 00007fe197604a50 0000000000000000 0000000000000000 E1000

                               R0 00007fe1bdd21d20 ffffffffa0dd2fb0 ffffffffa0dd2fd0 0000000000000000 0000000000000000

                               RC 00000000 00000000 00000000 00000000 00000000

00000000fec00000-00000000fec00fff 00007fe1bdd17230 00007fe197315ce0 00007fe1973163b0 0000000000000000 00007fe1bdd17330 I/O APIC Memory

                               R0 00007fe1bdd17230 ffffffffa0de3c30 ffffffffa0de3d40 0000000000000000 0000000000000000

                               RC 00000000 00000000 00000000 00000000 00000000

00000000fed00000-00000000fed00fff 00007fe1bdd17650 00007fe1975ba020 00007fe1975b9c00 0000000000000000 00007fe1bdd17750 HPET Memory

                               R0 00007fe1bdd17650 ffffffffa0dcc540 ffffffffa0dcc7a0 0000000000000000 0000000000000000

                               RC 00000000 00000000 00000000 00000000 00000000

00000000fed1c000-00000000fed1ffff 00007fe1ac55c580 00007fe197316980 00007fe197316c60 0000000000000000 00007fe1ac55c680 LPC Memory

                               R0 0000000000000000 0000000000000000 0000000000000000 0000000000000000 0000000000000000

                               RC 00000000 00000000 00000000 00000000 00000000

00000000fee00000-00000000fee00fff 00007fe1bdd16020 00007fe197313be0 00007fe197315a80 0000000000000000 00007fe1bdd16120 APIC Memory

                               R0 00007fe1bdd16020 ffffffffa0de3a10 ffffffffa0de3ab0 0000000000000000 0000000000000000

                               RC 00000000 00000000 00000000 00000000 00000000